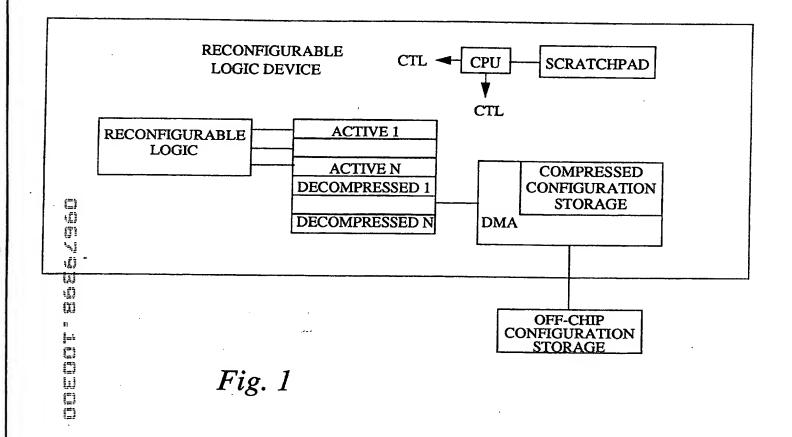
Blakely, Sokoloff, Taylor & Zafman LLP (503) 684-6200 Title: Hierarchical Storage Architecture for Reconfigurable Logic

Configurations

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2	1	1	9
1	7	7	7
0	1	3	9
Y: X: 0		1	2

Fig. 2a

110 _b 2 1 100 _b 0 1 010 _b 0 3 111 _b 1 7 001 _b 0 9
COMPRESS

Fig. 2b

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	CONTENT		
CONFIGURATION	OFF-CHIP ADDR.	TAG (T/F)	₩ ¬
			CNTR.
CONFIGURATION	OFF-CHIP ADDR.	TAG (T/F)	4 -

Fig. 3

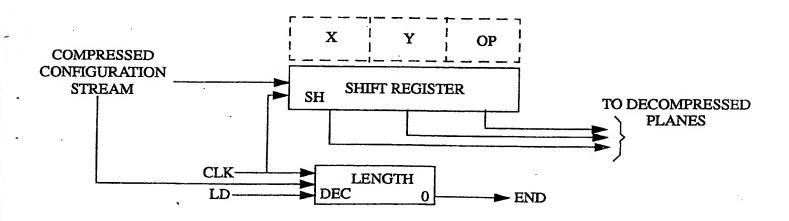


Fig. 4

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SCRATCHPAD

PLANE	CONFIG. ADDR.
0	A ₀
1	A_1
N	A_N

Fig. 5

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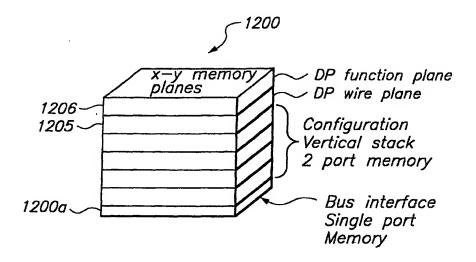


FIG. 6a

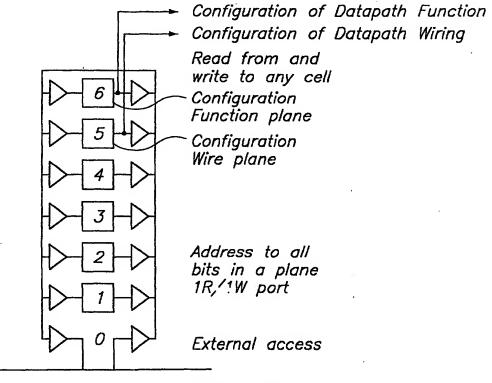


FIG. 6b

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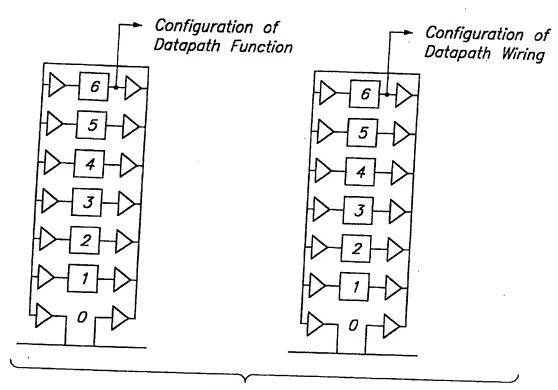


FIG. 6c

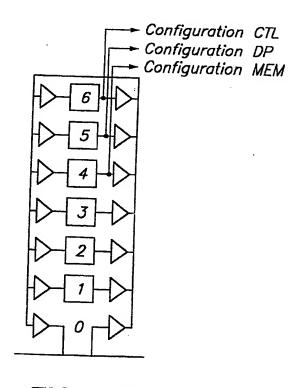


FIG. 6d

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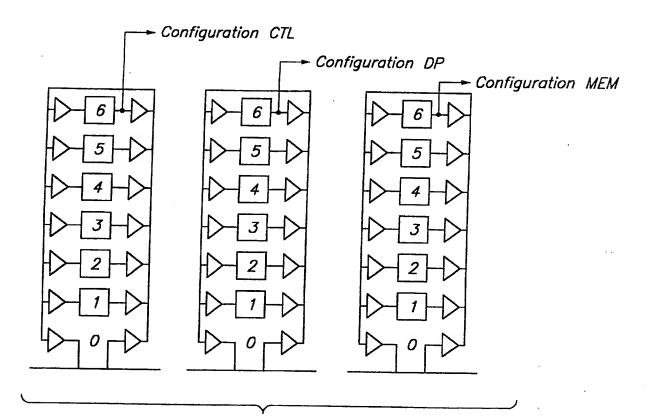


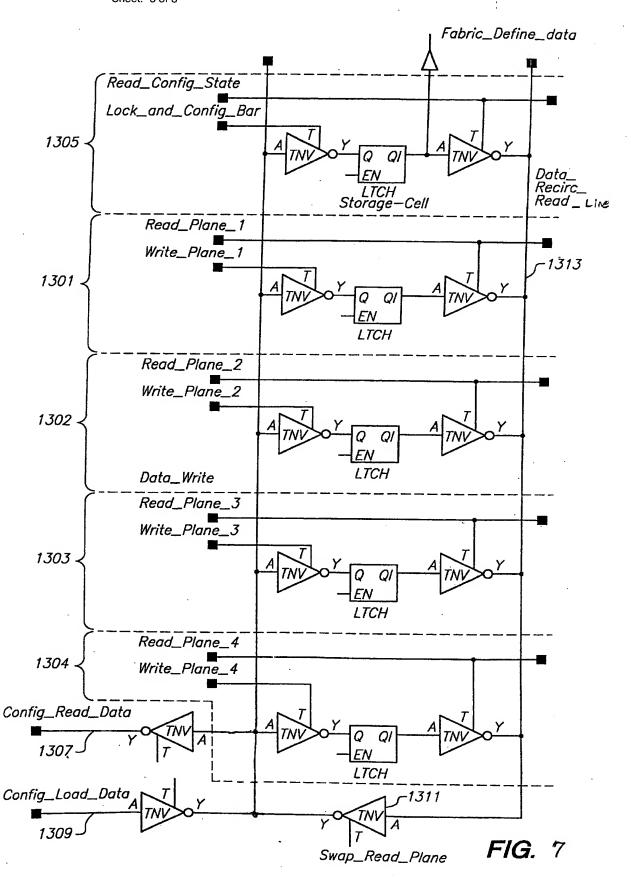
FIG. 6e

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